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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/537,891	06/07/2005	Wojtek Sudol	US020535US	7204
24737 PHILIPS INTE	7590 10/31/200 LLECTUAL PROPER	EXAMINER		
P.O. BOX 3001			LAMPRECHT, JOEL	
BRIARCLIFF	RIARCLIFF MANOR, NY 10510		ART UNIT	PAPER NUMBER
			3737	
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			10/31/2007	PAPER

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

	Application No.	Applicant(s)			
•	10/537,891	SUDOL ET AL.			
Office Action Summary	Examiner	Art Unit			
	Joel M. Lamprecht	3737			
The MAILING DATE of this communication appears on the cover sheet with the correspondence address Period for Reply					
A SHORTENED STATUTORY PERIOD FOR REPL WHICHEVER IS LONGER, FROM THE MAILING D  - Extensions of time may be available under the provisions of 37 CFR 1. after SIX (6) MONTHS from the mailing date of this communication.  - If NO period for reply is specified above, the maximum statutory period  - Failure to reply within the set or extended period for reply will, by statute Any reply received by the Office later than three months after the mailin earned patent term adjustment. See 37 CFR 1.704(b).	PATE OF THIS COMMUNICATION 136(a). In no event, however, may a reply be tir will apply and will expire SIX (6) MONTHS from e, cause the application to become ABANDONE	N. nely filed the mailing date of this communication. D (35 U.S.C. § 133).			
Status					
<ul> <li>1) ⊠ Responsive to communication(s) filed on 01 A</li> <li>2a) ⊠ This action is FINAL. 2b) ☐ This</li> <li>3) ☐ Since this application is in condition for allowated closed in accordance with the practice under A</li> </ul>	s action is non-final. ance except for formal matters, pro				
Disposition of Claims					
4)  Claim(s) 1-27 is/are pending in the application 4a) Of the above claim(s) is/are withdra 5)  Claim(s) is/are allowed. 6)  Claim(s) 1-27 is/are rejected. 7)  Claim(s) is/are objected to. 8)  Claim(s) are subject to restriction and/o	wn from consideration.				
Application Papers					
9) The specification is objected to by the Examine 10) The drawing(s) filed on is/are: a) accomposed and applicant may not request that any objection to the Replacement drawing sheet(s) including the correct of the oath or declaration is objected to by the E	cepted or b) objected to by the drawing(s) be held in abeyance. Section is required if the drawing(s) is ob	e 37 CFR 1.85(a). jected to. See 37 CFR 1.121(d).			
Priority under 35 U.S.C. § 119					
<ul> <li>12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).</li> <li>a) All b) Some * c) None of:</li> <li>1. Certified copies of the priority documents have been received.</li> <li>2. Certified copies of the priority documents have been received in Application No</li> <li>3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).</li> <li>* See the attached detailed Office action for a list of the certified copies not received.</li> </ul>					
Attachment(s)  1) Notice of References Cited (PTO-892)  2) Notice of Draftsperson's Patent Drawing Review (PTO-948)  3) Information Disclosure Statement(s) (PTO/SB/08)  Paper No(s)/Mail Date	4) Interview Summary Paper No(s)/Mail D 5) Notice of Informal F 6) Other:	ate			

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## **DETAILED ACTION**

### Response to Amendment

# Response to Arguments

1. Applicant's arguments filed 8/1/07 have been fully considered and the amendments to change the scope of the claims have been entered and examined. Regarding the argument and amendment to Claim 1, that is, "connection sites for said first and second connection means and said acoustic elements being arranged on a common surface of said integrated circuit", Examiner disagrees. With reference to Hadjicostis Figures 3a, 4a-8b Examiner cites that Hadjicostis discloses connection means (4a) and acoustic elements (7a) being disclosed on the same surface of the integrated circuit (3a). The connection bundle is cylindrically arranged as is the acoustic stack, giving a connection to the outer edge surface of the flex circuit. Applicant's amendments with respect to Claims 8 and 26 have necessitated further search and the arguments are rendered moot in view of the new grounds of rejection (and in turn dependant claims 6, 12 and 27). Applicant's arguments and amendments with respect to claim 5 have been fully considered but are not persuasive. Claim 5 denotes an intermediate substrate with rigid and flexible portions. Hadjicostis discloses supporting members on the MUXes, which are rigid (element 42b) in nature along that portion of the circuit and further down the length of the line the circuit is flexible and still includes intermediate interconnection substrates (Fig 3a, 4a, 7a) (22a, 20a, etc)).

#### Claim Rejections - 35 USC § 102

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

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1. Claims 1-5, 23-25 are rejected under 35 U.S.C. 102(b) as being anticipated by Hadjicostis et al (US 5,947,905). Hadjicostis et al disclose an ultrasonic transducer with a housing (Fig 1-8), acoustic elements (Fig 7b, Col 5 Line 60 – Col 6 Line 10), an integrated circuit adjacent to the acoustic elements (Col 5 Line 60 - Col 6 Line 10), a first connection means and second connection means for connecting the acoustic elements to an integrated circuit and connection that integrated circuit to electrical transmission lines on a common surface (Col 5 Line 60 - Col 6 Line 55), where the first connection means are comprised of one of metal bumps, solder bumps, polymer bumps, reflow solder, z-axis conductive adhesive, z-axis conductive film or z-axis conductive elastomeric connectors and the second connection means is comprised of one of wire-bonds, direct wire attachments and tab bonding of leads (Col 7 Line 1-65). Hadjicostis et al also disclose interconnection substrate for the second connection means comprising a thin film circuit (Col 7 Line 35-50), ceramic circuit, or laminate circuit, an intermediate interconnection substrate comprising a flexible circuit, and a semi-rigid circuit or a rigid circuit, a bent interconnection, so that the horizontal length is less than fifty percent of a horizontal length of the integrated circuit (Col 9 Line 10 – Col 10 Line 30). Hadjicostis et al disclose an ultrasonic transducer with a thermally coated body (Col 7 Line 1-40), flexible circuit with electronic components on one portion and acoustic components on another portion of the circuit contacting the body (Col 6 Line 50 - Col 7 Line 15, Col 8 Line 10 - 65), the acoustic assembly including acoustic elements and an integrated citcuit coupled to the acoustic elements (Col 8 Line 10 - Col 9 Line 37), with two rows of wire-bonds along each pair of opposed edges (Col 6 Line 10 - 40).

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The flexible circuit having two planar potions on opposite sides of a body, connection means with two additional flexible circuits with connections for signal transmission lines, and a flap portion separated from the first planar portion and connections means with one additional flexible circuit having connections for signal transmission lines and conductive film or adhesive attaching the additional flexible circuit to the flap potion of the circuit (Col 5 Line 10- Col 6 Line 50).

Regarding claims 23-25 Hadjicostis et al disclose a transducer with a flexible circuit having connection sites (Col 5 Line 10-40), an acoustic assembly mounted on a flexible circuit and comprising an integrated circuit having connection sites and acoustic elements electrically coupled to an integrated circuit (Col 7 Line 5-54), electronic components for control of the acoustic assembly connected in a circuit defined in part by a flexible circuit (Col 8 Line 14 – Col 9 Line 10), two wire-bonds connecting the connection sites of the integrated circuit and the connection sites of the flexible circuit along each opposed edge.

# Claim Rejections - 35 USC § 103

- 2. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
  - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 3. Claim 6-22 and 26-27 are rejected under 35 U.S.C. 103(a) as being unpatentable over Hadjicostis et al in view of Marian Jr (US 7,022,080 B2). Hadjicostis et al fails to disclose that a portion of the interconnection extends in a first direction along a

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communication means and then extends at least perpendicularly in a second direction along the connection means or that bends at least at a perpendicular angle to define some cavity which contains thermally conductive elements and defining a transmission line which connects signal lines to electric components and the acoustic assembly. Attention is directed to Marian Jr which describes the use of a flexible circuit having at least a perpendicular bend which in part creates a cavity around thermally conductive elements connected to the circuit which has both flexible and rigid portions (Figure 2, Col 3 Line 30-Col 4 Line 45). While designs are chosen for both aesthetic, machining, and functional properties and therefore are never identical, the flexible circuit with a bendable element of Marlan Jr would have been obvious to one of ordinary skill in the at the time of the invention for use in conjunction with the system of Hadjicostis et al for the purpose of providing for a cost-efficient, connectively flexible transducer element which is able to electrically couple more elements more efficiently along the electronic pathway.

#### Conclusion

4. **THIS ACTION IS MADE FINAL.** Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any

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extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of this final action.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Joel M. Lamprecht whose telephone number is (571) 272-3250. The examiner can normally be reached on Monday-Friday 7:30AM-4PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Brian L. Casler can be reached on (571)272-4956. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

JML 10/22/07

SUPERMISORY PATENT EXAMINER